

**REMARKS**

Claims 1 and 6 are amended herein. Upon entry of this amendment, claims 1-19 will be pending.

Enclosed is a Credit Card Payment Form authorizing payment of the fee for a one month extension of time.

**Section 102**

Applicant respectfully requests reconsideration of the rejection of claims 1-8 and 10-19 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,369,789 (Ulrich).

Claim 1 recites a liquid crystal display drive method, said liquid crystal display comprising a first electrode and a second electrode, wherein pictures are displayed by means of a voltage signal impressed between said first and second electrodes, wherein a drive voltage waveform consisting of a display signal period and a control signal period irrelevant to display is used, and wherein said drive voltage waveform consists of a predetermined number of bit planes and the control signal period occurs during at least one preselected bit plane within the predetermined number of bit planes.

Claims 6-8 and 10-19 recite a liquid crystal display method, said liquid crystal display comprising a first electrode, and a second electrode, wherein pictures are displayed by means of a voltage signal impressed between said first and second electrodes, wherein a drive voltage waveform consisting of a display signal period and a control signal period irrelevant to display is used, wherein said drive voltage waveform consists of predetermined number of bit planes and the control signal period occurs during at least one preselected bit plane within the predetermined number of bit planes, and wherein one of the two states, either on or off, is chosen to select the state of incident light in said display signal period.

In contrast to the claims, Ulrich discloses a method of reducing the effects of ionic memory in an FLC material to which a switching pulse is applied comprising the steps of adding ionic dopant to the FLC material, and following the switching pulse with a TRIFLE pulse of opposite polarity. Ulrich does not disclose a method in which a drive voltage waveform consists of predetermined number of bit planes and the control signal period occurs during at least one preselected bit plane within the

predetermined number of bit planes. Because every element recited in the claims is not found in a single reference, the Section 102 rejection is improper and should be withdrawn. Accordingly, applicant respectfully requests the Section 102 rejection be withdrawn.

### Section 103

Applicant respectfully requests reconsideration of the rejection of claim 9 under 35 U.S.C. § 103(a) as being unpatentable over Ulrich in view of U.S. Patent No. 6,417,828 (Sato) and U.S. Patent No. 6,057,878 (Ogiwara).

Claim 9 recites a liquid crystal display drive method, said liquid crystal display comprising a first electrode, and a second electrode, wherein pictures are displayed by means of a voltage signal impressed between said first and second electrodes to select one state of incident light, wherein a drive voltage waveform consisting of a display signal period and a control system period irrelevant to display is used, wherein said drive voltage waveform consists of a predetermined number of bit planes and the control signal period occurs during at least one predetermined bit plane within the predetermined number of bit planes, and wherein one of the two states, either on or off, is chosen to select the state of incident light in said display signal period.

As discussed above, Ulrich fails to disclose a drive voltage waveform consisting of a predetermined number of bit planes and a control signal period occurring during at least one preselected bit plane within the predetermined number of bit planes. The secondary references also fail to disclose or suggest this recited feature. Accordingly, the Section 103 rejection is improper and should be withdrawn.

### Conclusion

As it is believed the application is in condition for allowance, a favorable action and Notice of Allowance are respectfully requested.

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Respectfully submitted,



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